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Inderscience Publishers: IJESDF-49165 - review reminder

1 message

Inderscience Submissions <submissions@inderscience.com>
To: ravi.se@velsuniv.ac.in

Sat, Aug 15, 2020 at 4:31 PM



Dear Dr. Ravikumar D,

Ref: Article entitled "FPGA Implementation of High Speed AES Based Authentication Algorithm".
Submission code: IJESDF-49165

Submitted to: Int. J. of Electronic Security and Digital Forensics

You recently accepted to review the above article, and this email is a gentle reminder to work on your review.

The review deadline is 11 Sep 2020.

If this date has passed, please note that this request is still open.

We understand that you will have other commitments, but we do wish to keep the peer-review system moving along in a timely manner.

To send us your review advice and recommendations, please complete the online evaluation form available at: <https://indersciencesubmissions.com/index.php?q=ulogin-cmF2aS5zZUB2ZWxzdW5pdj5hYy5pbG==6-49165--show>Alternatively, you can log in and you will see the article listed in your review list.
You will also find some Reviewer guidelines there.

If you feel you are no longer able to take on this review, please let us know as soon as possible by using the 'Send a comment to the Editor' function in the online submissions system.

With kind regards,

Prof. Hamid Jahankhani (Editor)
Int. J. of Electronic Security and Digital Forensics
<http://www.inderscience.com/IJESDF>
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